

BNL-104752-2014-TECH

AGS/AD/Tech Note No. 336;BNL-104752-2014-IR

RLRM SYSTEM DIAGNOSTICS

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April 1990

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U.S. Department of Energy

USDOE Office of Science (SC)

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> Accelerator Division Technical Note

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RLRM SYSTEM DIAGNOSTICS

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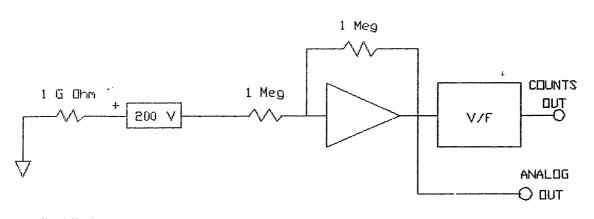
April 11, 1990

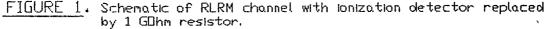
A bank of 16 resistors, each 1 GQ, 5% tolerance, is mounted at the RLRM racks in the RF Building. Figure 1 is a schematic of one channel of the RLRM system showing a 1 GQ resistor in place of the detector. Connecting these resistors in place of the detectors produces a simulated signal of 200 nA (200 V/1 GQ). With the resistors in, the analog signals available via XBAR show a d.c. offset of 200 mV (feedback resistor = 1 MQ). The digital signals for these channels via the Fortran program RLRM have a mean value of about 1400 counts per second, with a standard deviation of 60.

The resistor bank is routinely used to check every RLRM channel before start up of operations, and when diagnostics are warranted. During normal operation, 7 of the resistors are connected to the first 7 spare channels. A resistor switched by a relay is normally connected to spare channel 8. Figure 2 is a printout from the RLRM program showing digital data for the spare channel.

System timing is checked via a relay driven by a 556 dual timer circuit, to switch a 1 G Ω resistor at the input to a selected channel. The timer is triggered at T_0 , starting a delay of 500 msec, before the relay is energized for 500 msec. This simulates a signal pulse of 200 mV starting 500 msec after T_0 , ending 1000 msec after T_0 . Net timer accuracy is approximately ± 10 msec. Figure 3 is a schematic of the relay timer circuit. Figure 4 is an oscilloscope trace photograph showing the pulsed signal from spare channel 8.

A Cesium-137 source at 160 mCi has been procured to aid in RLRM detector testing and calibration. As a test, the source was positioned in contact with 9 RLRM detectors in the E superperiod, using a 200 V bias supply and a picoammeter to measure the current produced. Each measurement consisted of a reading of the ionization current with the source extended, less the reading with the source in the shielded position in the source holder. The mean and standard deviation for the two measurements on each of the nine detectors was 43 ± 3 pA. (Note that this current when digitized by the V/F can produce only 0.3 counts/second.) It is planned that all the detectors will be calibrated at the end of the summer shutdown, when residual activity is minimum.





U-P SET UP: MDDE=1 500 TD 1500MS 13-MAR-90 TIME=10:06 04.0							PLOT NUMBER			1 D	ISPLAYED	
A 1 1 1 1 1 1 1 1 1	B 0 1 1 1 1 1 0 1 1	C 1 1 1 1 0 1 0 1	D 1 1 1 1 1 1 0 1	E 1 1 1 1 1 1 20 113	418 0 894 64 73 445 103 183 67	63 10 1 1 1 1 2 10 10	H 1 1 1 2 1 56 1	· I 16 779 1 1 1 1 5 0 11	J 1 1 1 1 1 1 1 1	K 1 1 1 1 1 1 1 1	L 0 1 1 1 1 1 1 1	1267 1339 1392 1411 1451 1424 1408 753
TOTAL = 3540 CBM AT 500MS =1770 CBM AT1500MS = 982 TOTAL COUNTS												
2 2 2 2 2 2 2 14 16 2	0 2 2 2 2 2 2 0 2 2	2 2 2 2 2 2 0 8 0 2 2	2 2 2 2 2 2 2 2 0 2 2 2	2 22 2 2 2	1743 0 2576 207 236 1416 328 541 226 304	185 41 2 2 2 2 9 20 2 2 2	4 2 2 21 21 161 2 0*	0* 63 2455 2 2 2 2 2 9 0 41	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	2 2 2 2 2 2 2 11 5 2	2 2 2 2 2 2 2 2 2 2 2 2	4014 4242 4409 4466 4594 4510 4461 755
TO	TAL =		11337									

Figure 2. Digital printout from RLRM showing data for spare hannels 1 to 7 with 1 G Ω resistors replacing detectors; mean is 1385, standard deviation is 62. Spare channel 8 is switched 1 G Ω resistor, in at 500 msec, out at 1000 msec.

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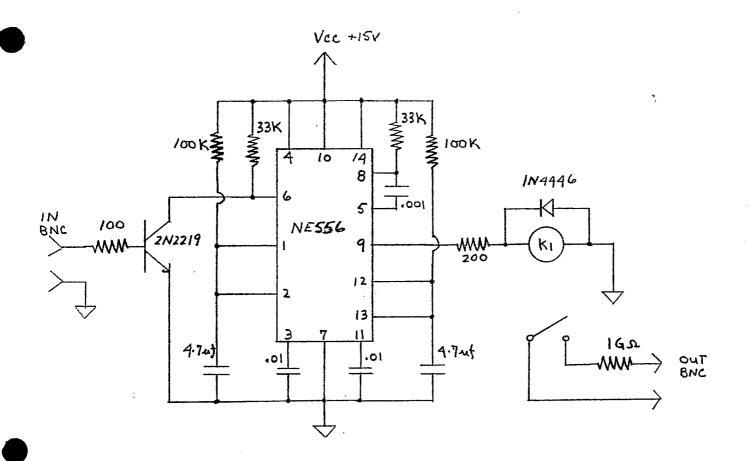
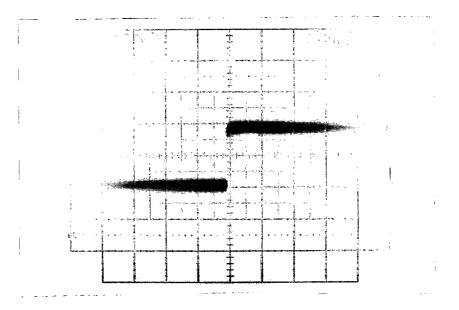


Figure 3. Schematic of relay timer circuit.



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Figure 4. Scope trace of RLRM spare channel 8.

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