

# OPERATION NOTES BOUSSARD BEAM COMPENSATION

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June 1984

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**U.S. Department of Energy**  
USDOE Office of Science (SC)

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AGS Division Technical Note  
No. 201

OPERATION NOTES  
BOUSSARD BEAM COMPENSATION

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June 29, 1984

The AGS is presently operating with active beam compensation at injection and extraction. The circulating beam in the machine will induce a voltage at the accelerating gap due to the beam interacting with the impedance of the synchronously tuned cavities. The induced voltage will cause unwanted bunching of the beam at injection and extraction. The effective impedance of the cavity to the beam is reduced to zero by adding to the cavity drive a properly phase shifted signal proportional to the circulating AGS beam. The technique of using additional beam feedback to achieve beam cancellation at the AGS was proposed by D. Boussard of CERN in the summer of 1983. This note will describe the operation of the prototype Boussard Beam Compensation (BBC) equipment. The BBC, as presently implemented, does not permit phase shifting of the signal during the accelerating cycle (rf frequency slewing from 2.5 to 4.5 MHz). The BBC is used only when the beam frequency is constant, i.e. injection or extraction.

Figure 1 is a simplified block diagram of the BBC. Figure 2 is a timing diagram of the BBC. The numbers in circles on Figure 1 refer to the timing signals in Figure 2.

The normal beam pick up at E20 feeds the Low Level RF (LLRF) as before. The output (2VP-P) of the LLRF is split into two channels. One output is the main channel input to the BBC. The other output goes to the RF Building, as before, to provide frequency-tuning current information to the Main Tuning Servo (MTS).

A second beam pickup at G7 provides a beam reference signal (amplitude and phase) to the BBC. Figure 3 is a block diagram of the

BBC unit. The beam pickup signal, which is proportional to the beam induced signal at the cavities is split four ways. A low-pass filter is used to remove the higher harmonics from the beam pickup. Three of the four signals feed phase shifting modules, while the fourth is used as a test port. The three phase-shifting modules are identical except for the input low-pass filter. This filter is used to suppress second and higher harmonics of the frequency being shifted. At present only two of the modules are being used, injection  $f \approx 2.50$  MHz and extraction  $f \approx 4.450$  MHz. The third module is for future use.

There is  $180^\circ$  of continuous phase shift control from the MCR. In addition, a switch on the module will provide fixed increments of  $0^\circ$ ,  $\pi/2$ ,  $\pi$ , and  $-\pi/2$  to insure smooth phase adjustment at any operating phase. Presently, the prototype has provision for  $0^\circ$  on  $\pi$  switching.

After the phase shifting, an adjustable gain amplifier is used to set the magnitude of the phase shifted feedback signal. The 45 db of gain adjust is computer controlled from the MCR. Input and output gating is used to prevent unwanted signal leakage at times other than the desired compensation time. The output of the phase shifters is added to the main channel signal. Signal 16 of Figure 2 illustrates the time superposition of the injection and extraction signals in the normal rf drive to the HLRF system.

The main channel signal is gain controlled for proper bunching and acceleration of the beam. An AGC amplifier compares the sum of the 10 cavities with the desired energy gain function and adjusts the gain of the main channel accordingly. The gain of the HLRF is fixed such that a 3 V PP input will drive the output of the 10 power amplifier outputs to 120 kW.

Figure 4 is a block diagram of the trigger generator for the BBC. Figure 5 illustrates the BBC modules and typical signals at the test ports of the modules.

mvh

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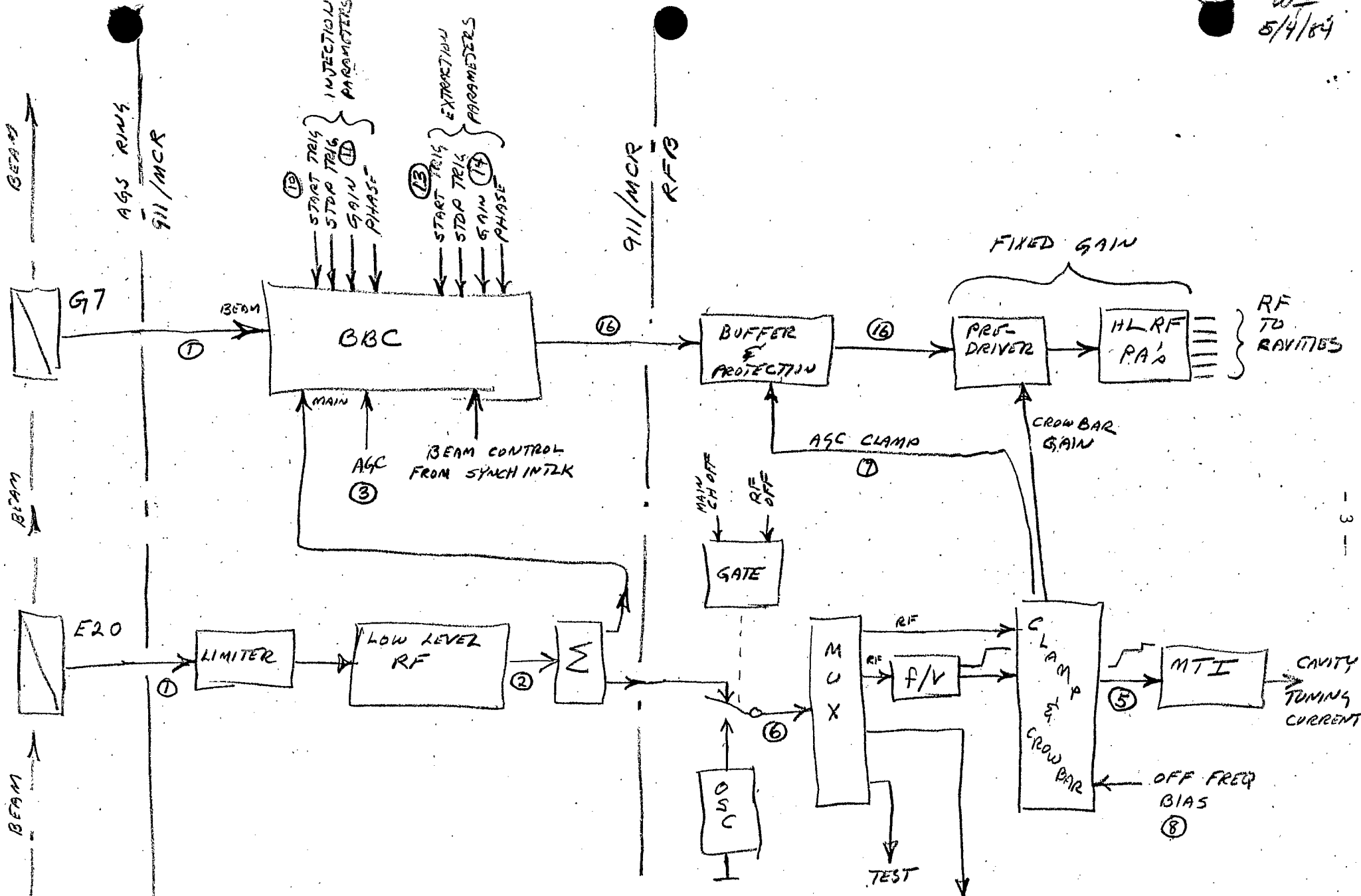
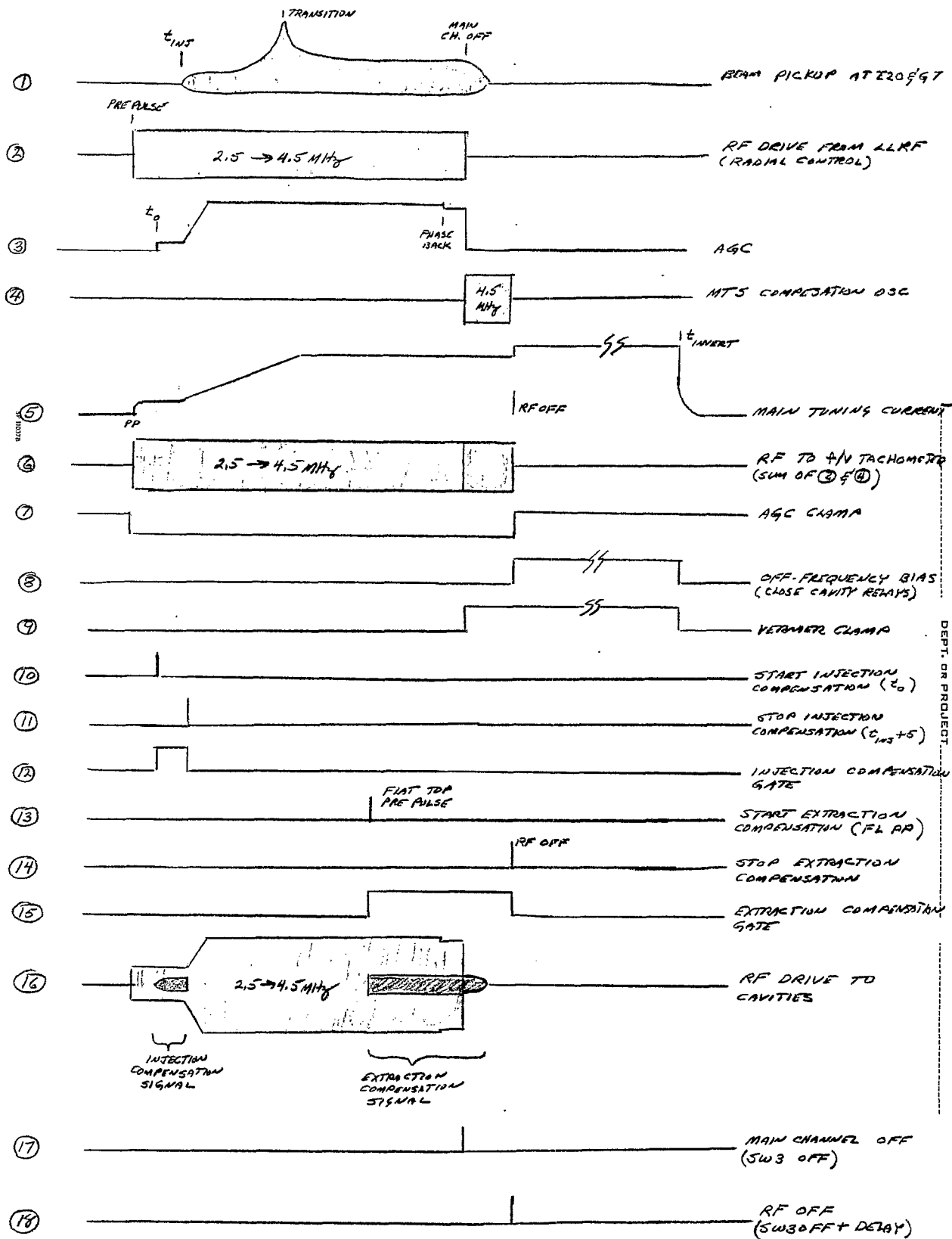


Figure 1 RF SYSTEM-  
BEAM COMPENSATION  
BLOCK DIAGRAM



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FIGURE 2  
 BBC TIMING DIAGRAM

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BY \_\_\_\_\_ DATE \_\_\_\_\_ SUBJECT BBCM COMPENSATION SHEET NO. \_\_\_\_\_ OF \_\_\_\_\_  
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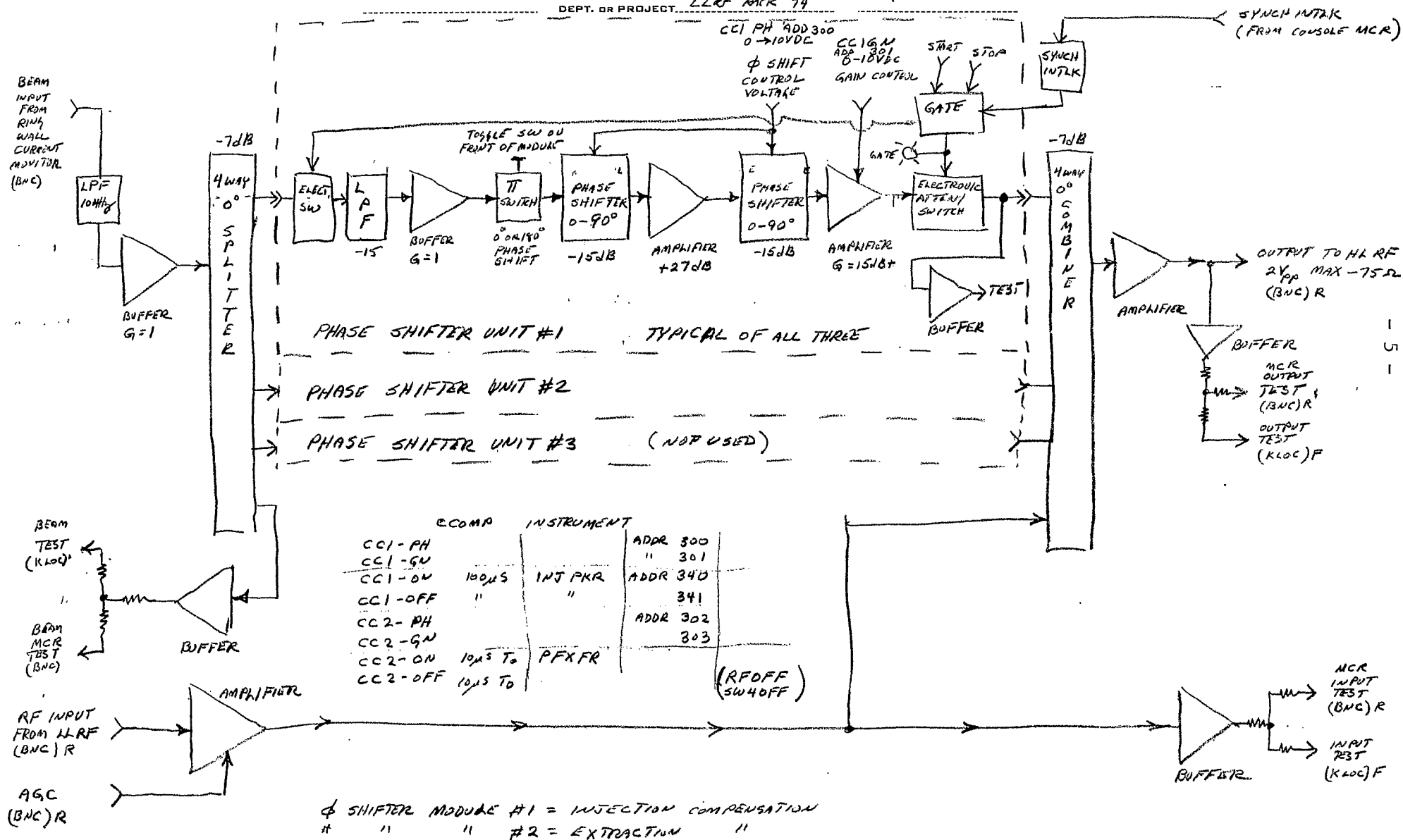


FIGURE 3  
BLOCK DIAGRAM - BBC UNIT

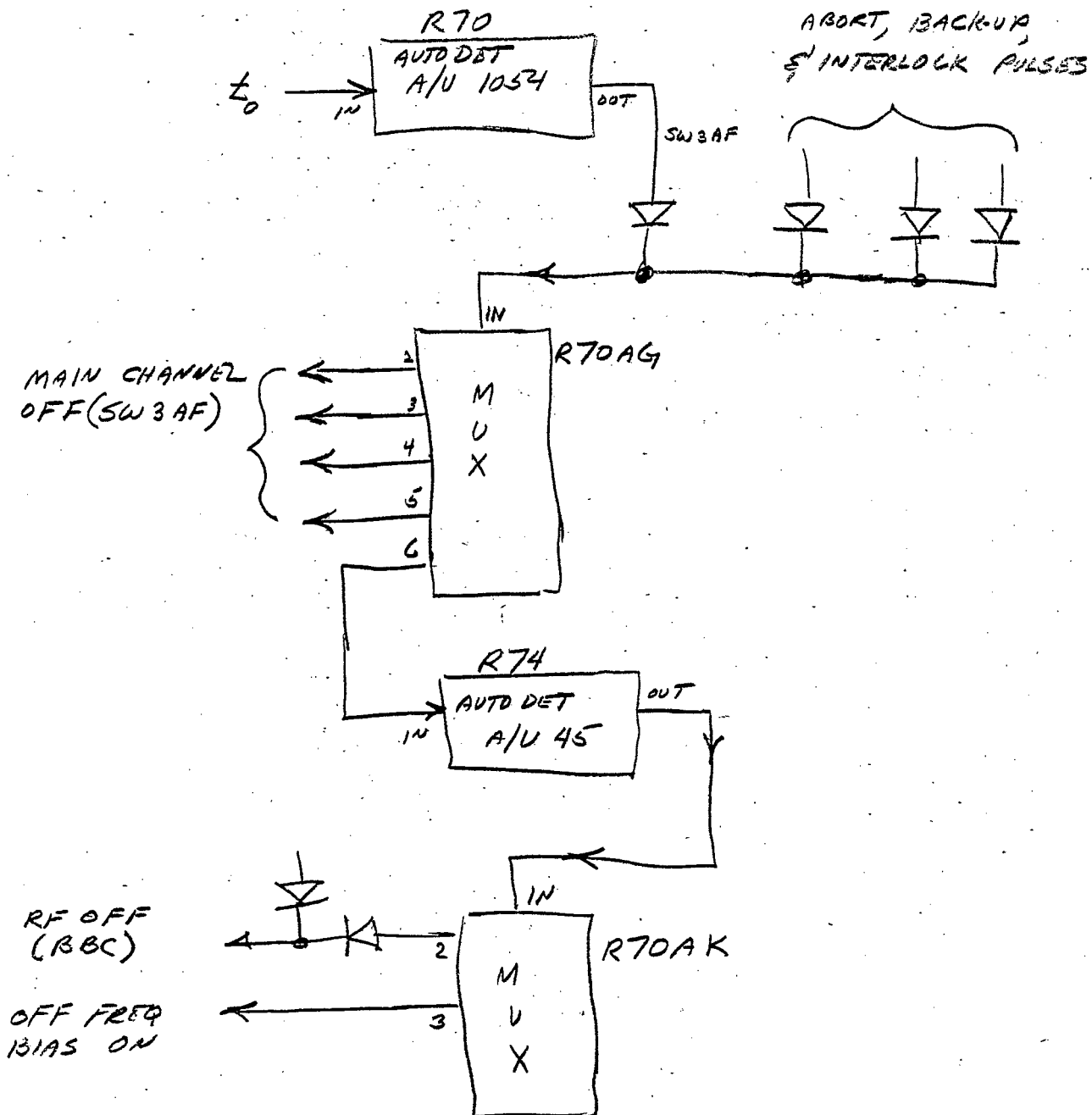


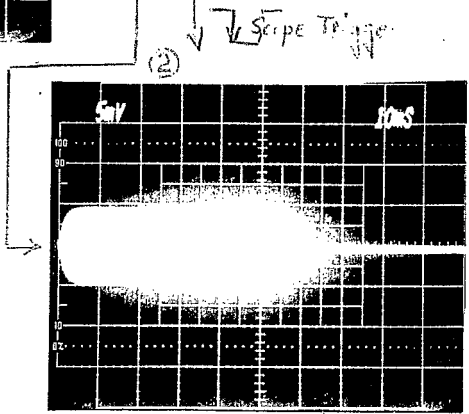
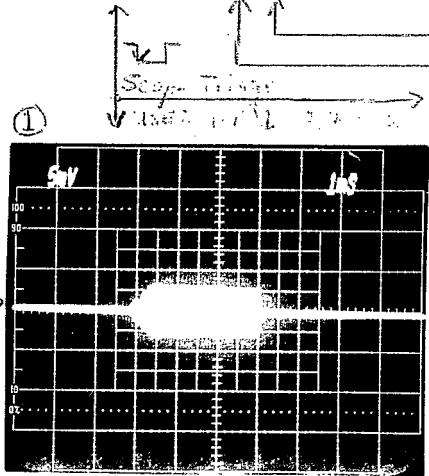
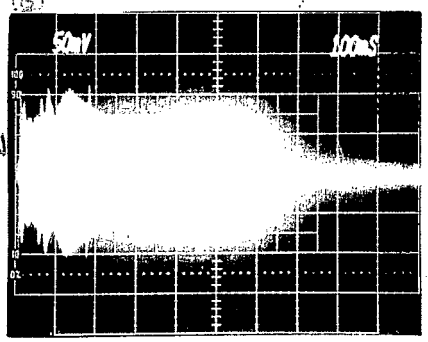
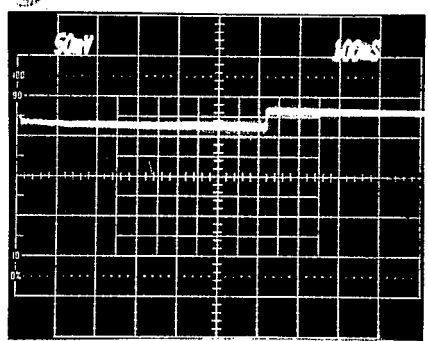
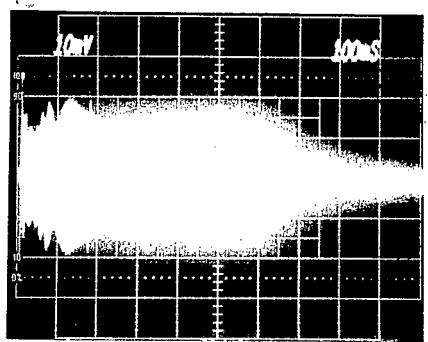
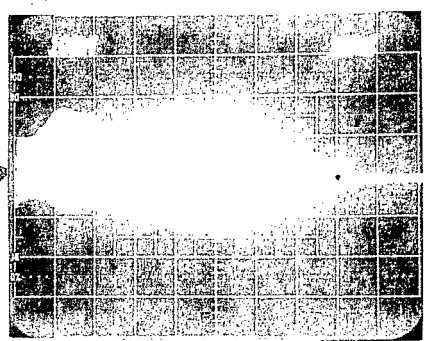
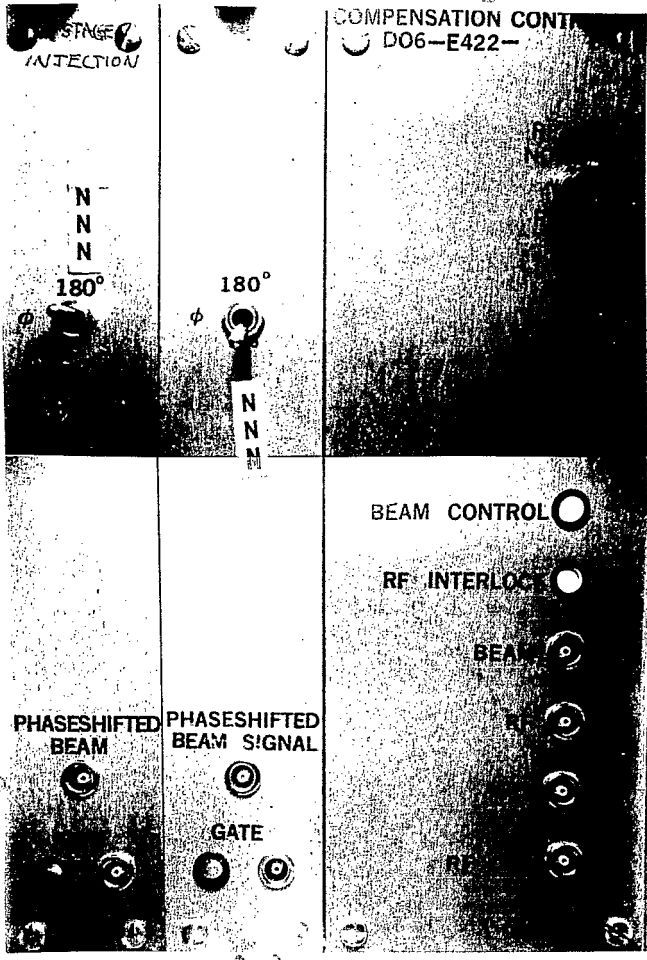
Figure 4  
TRIGGER SOURCE  
DIAGRAM



FIGURE 5

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ALL SIGNAL TRACES  
R06-2, 100ns Terminated